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CONFIRMATION NO. 1155

| SERIAL NUMBER | FILING or 371(c) DATE RULE | CLASS | GROUP ART UNIT | ATTORNEY DOCKET NO. |
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| 10/652,135 | | 712 | 2181 | CML00769D |

APPLICANTS

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**** CONTINUING DATA *******

This application is a CIP of 10/184,772 06/28/2002 PAT 7,140,019
 and is a CIP of 10/184,583 06/28/2002 PAT 7,159,099

**** FOREIGN APPLICATIONS *********** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ****

11/19/2003

| Foreign Priority claimed | <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No | <input type="checkbox"/> Met after Allowance TLMD | STATE OR COUNTRY IL | SHEETS DRAWINGS 9 | TOTAL CLAIMS ----- 24 17 | INDEPENDENT CLAIMS 3 |
|--|---|--|------------------------|----------------------|-----------------------------------|-------------------------|
| 35 USC 119(a-d) conditions met | <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No | | | | | TLMD |
| Verified and /TONIA LYNN MEONSKE DOLLINGER/ Acknowledged Examiner's Signature | | Initials | | | | 4/21/08 |

ADDRESS

LEVEQUE INTELLECTUAL PROPERTY LAW, P.C.
 221 EAST CHURCH ST.
 FREDERICK, MD 21701
 UNITED STATES

TITLE

Method and apparatus for elimination of prolog and epilog instructions in a vector processor

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|------------------------------------|---|---|
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